Morrow et. al.

YOR9-2000-0472US1(RPT)(8728-416)

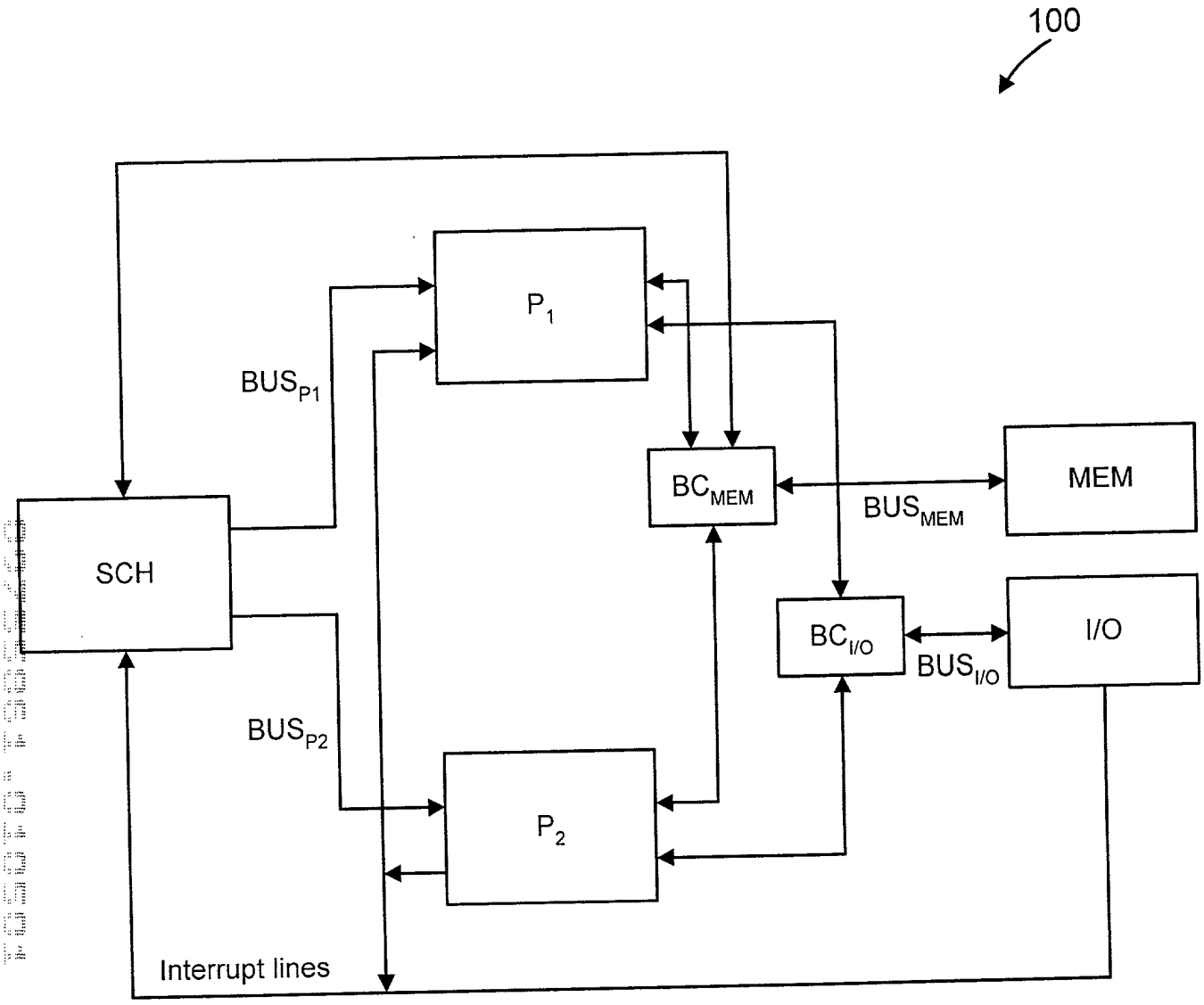

FIG. 1

YOR9-2000-0472US1 (8725-416)

Parameter	*Type*	*Unit*	*Description*
TID	word	n/a	Task IDentification number
NP	byte	n/a	Number of potential host processing units
$P_{TID,1}$	byte	n/a	ID of most energy efficient processor
$CPS_{TID,P_{TID,1}}$	word	[kHz]	Required cycles/sec to sustain task
$ADDR_1$	void*	n/a	Pointer to task code
.			
.			
.			
$P_{TID,NP}$	byte	n/a	ID of least energy efficient processor
$CPS_{TID,P_{TID,NP}}$	word	[kHz]	Required cycles/sec to sustain task
$ADDR_{NP}$	void*	n/a	Pointer to task code

FIG. 2

Parameter	*Type*	*Unit*	*Description*
N	byte	n/a	Number of processing units
CPS_1	word	[kHz]	Currently available cycles/sec
.			
.			
.			
CPS_{NP}	word	[kHz]	Currently available cycles/sec

FIG. 3

YOR9-2000-0472US1 (8728-416)

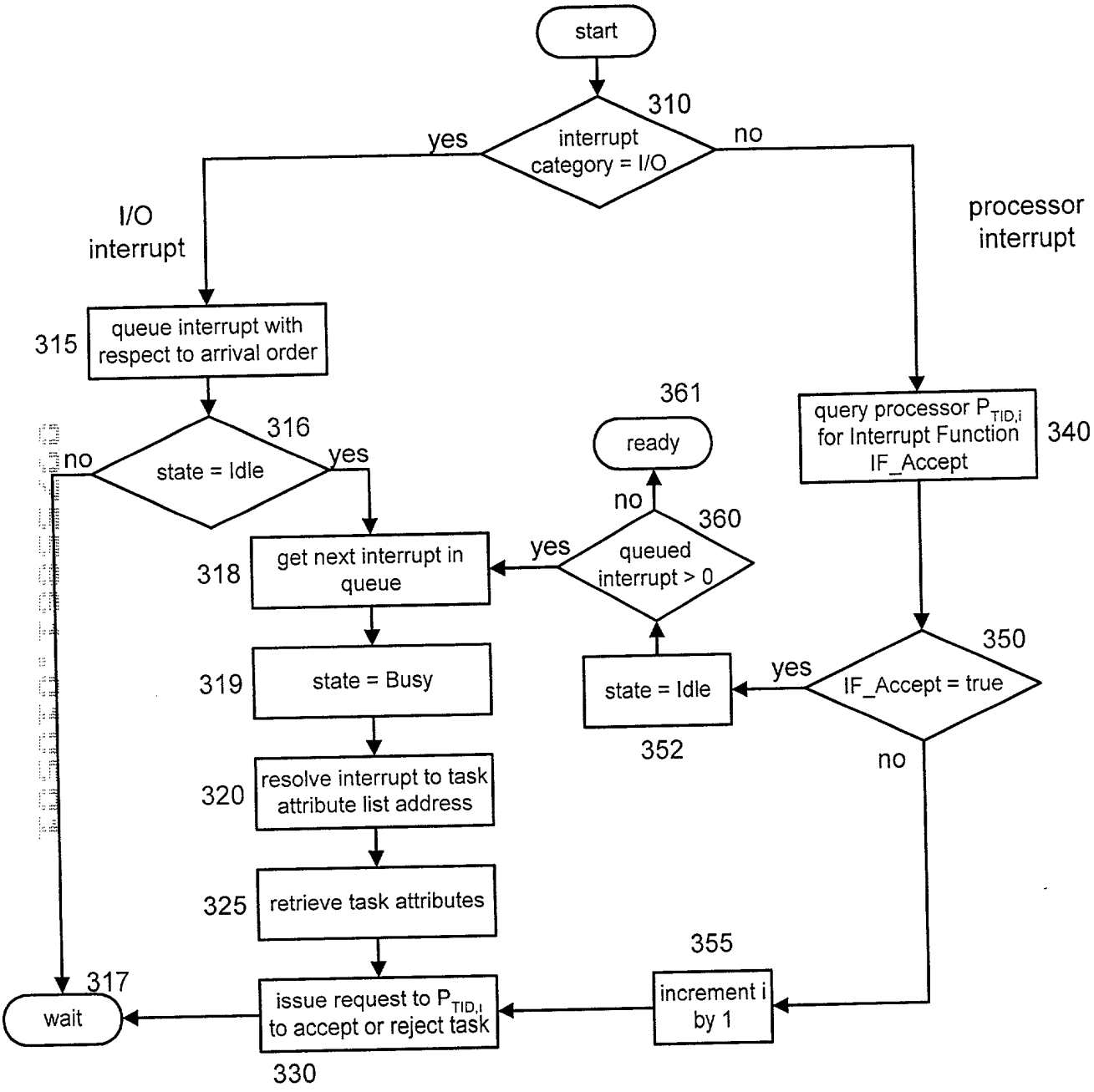

FIG. 4

YOR9-2000-0472US1 (8728-416)

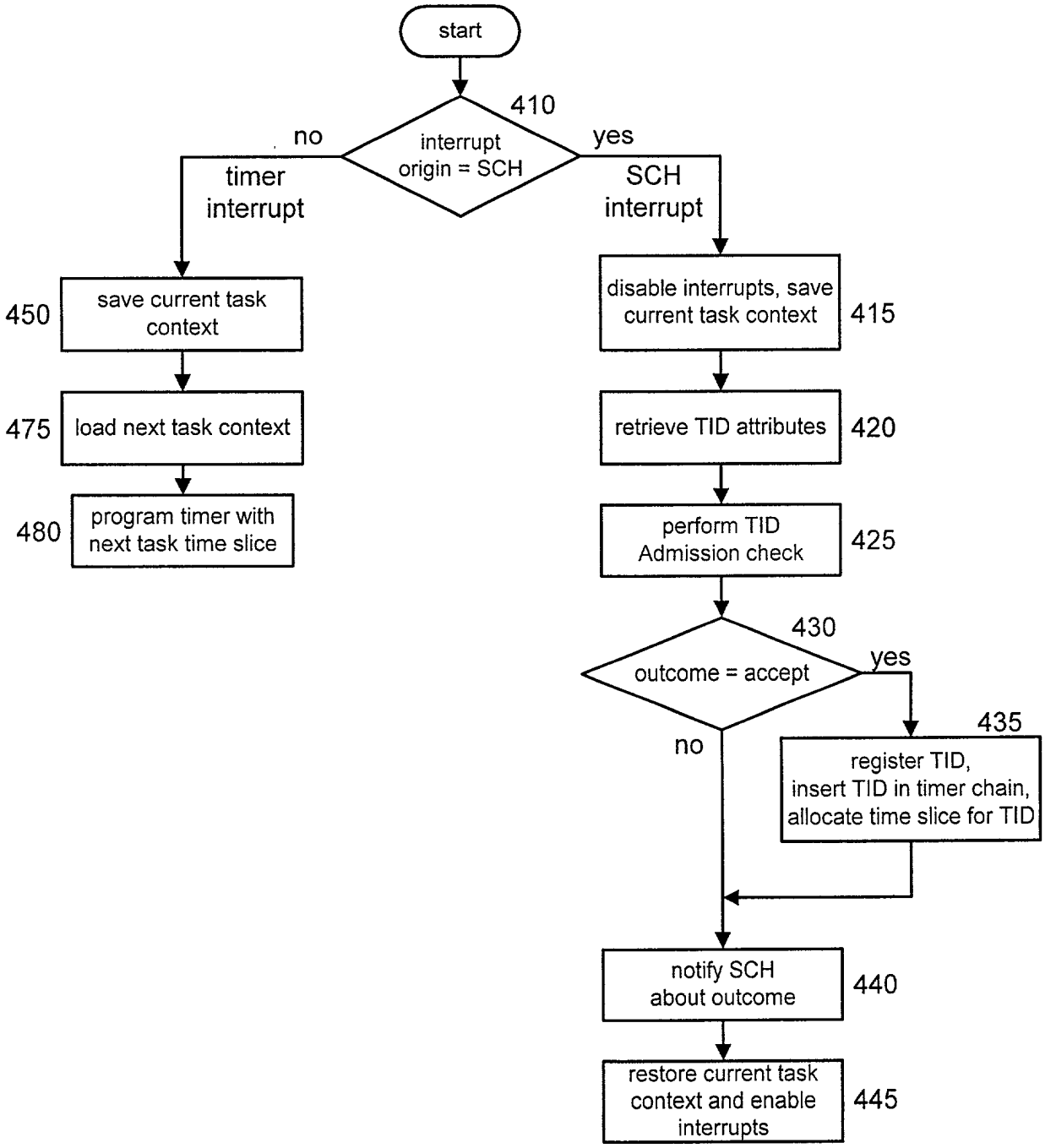

FIG. 5